



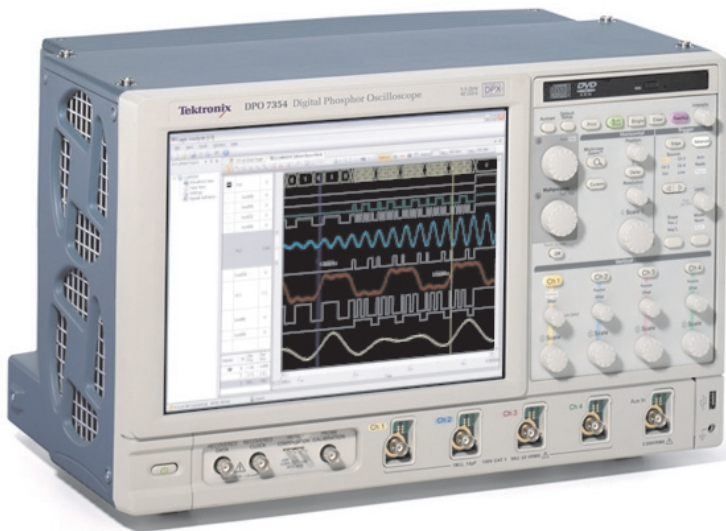
AT-LA500 for Mixed signal testing

Connect your oscilloscope to AT-LA500 to make a unique Mixed Signal Tester

AT-LA500 is a professional logic analyzer that can be tightly integrated with a standard oscilloscope providing the best mixed signal solution both in term of performance and price.

In mixed signal tests some features are as much important as signal bandwidth, sampling rate and memory depth: channel count, expandability, trigger levels, probe features, analogue-digital synchronization.

Many good oscilloscopes or mixed signal oscilloscopes are available at a very effective price. But as good are the performances of the analogue part as poor are those of the digital part which usually is not adequate for professional tests.



AT-LA500 completes a standard oscilloscope providing all the digital features required to carry out any mixed signal test situation:

- High sampling rate: 500Mps timing analysis, 200Mhz DDR state analysis
- Channel expandability: from 36 up to 288 channels
- 31 trigger levels
- Up to 4 Million Samples on all channels
- 3 different sets of probes with:
 - High bandwidth
 - Multiple independent programmable thresholds
 - High resistive (1M Ω) input impedance

AT-LA500 can be easily and efficiently integrated with a standard oscilloscope thanks to its very compact size



and the USB connection to the oscilloscope itself (for Windows based scopes) or to a PC which controls and displays the digital/analogue signals together.



AT-LA500 provides a dedicated expansion bus, called AT-XSS, for expandability and synchronization with external instruments (i.e. oscilloscopes).

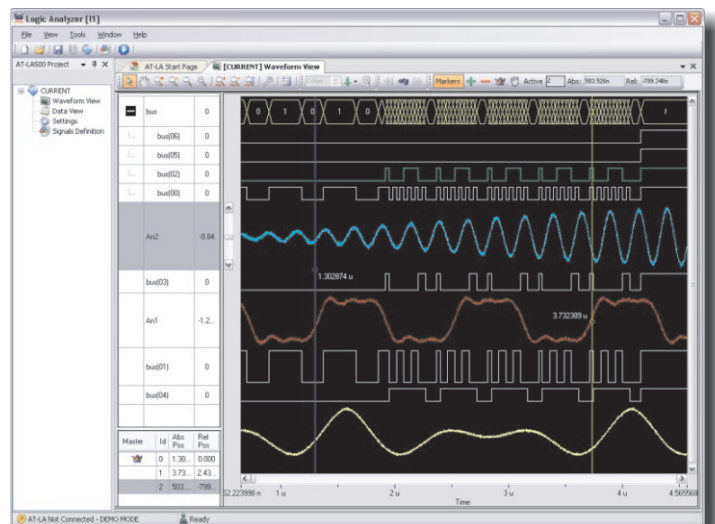
The synchronization is bidirectional so that the oscilloscope and the logic analyzer can be, in turn, the trigger master. Only two BNC cable connections are required to make the new mixed signal tester.



The integration between the oscilloscope and AT-LA500 is easily made by means of a powerful and very user-friendly software which hides all the details of the communication between the two instruments.

The mixed signal acquisitions can be displayed in the Mixed Window where it is possible to:

- Simultaneously display analogue and digital traces
- Add, remove, drag&drop signals
- Expand/collapse busses
- Perform advanced pattern/edge searches
- Add cursors and measure times
- Resize the analogue and digital views
- Print the analogue/digital waveforms



Active Technologies
www.activetechnologies.it

Tech Specs:

Channels:	36 (up to 288 channels with AT-XSS bus)	
Linkable instruments with AT-XSS bus:	Up to 8	
Maximum sampling frequency:	500MSamples/s	
Timing Analysis:	500MHz	
State Analysis:	200MHz DDR / 100MHz SDR It is possible to sample on an external clock that comes from different combination of 4 dedicated inputs	
Memory Depth:	Up to 4M Samples	
Trigger Settings:	Edge Condition:	No edge, Rising Edge, Falling Edge, Both Edges on all channels
	Level Condition:	Condition verified if the selected inputs are equal (or different) to the pattern that the user sets
Trigger Levels	31	
Trigger Mode:	Edge AND Level; Edge OR Level; Edge THEN Level; Level THEN Edge; Always Trigger; Never Trigger; Manual Trigger	
Dimensions (WxLxH):	17.3 x 27.3 x 6.7 cm	
Weight:	700g	
Interface:	USB 2.0 (compatible with USB 1.1)	
Power Supply:	12 VDC	

<i>Probes Tech Specs:</i>	<i>Active Hi-Z Probe</i>	<i>ASP Probe</i>	<i>Passive Probe</i>
Input Capacitance:	9pF	< 10 pF	//
Input Resistance:	1.1M Ω	22k Ω	//
Maximum Toggle Rate:	80MHz	130MHz	100MHz
Linear Input Voltage Range:	-40V to +40V	-38V to +38V	0 to 5V
Threshold Voltage:	-40V to +40V in 20mV steps	-38V to +38V In 20mV steps	2V
Number of Thresholds:	2 programmable and independent	2 programmable and independent	1 Fixed

AT-LA500 is a test equipment instrument designed and made in Italy by Active Technologies. The company was founded in 2002 by a staff of engineers expert in semiconductor test equipment and instrumentation design.

Active Technologies is a supplier of innovative and avant-garde Automated Test Equipment and electronic instrumentation to world wide semiconductor company leaders.

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